AVNEESH YADAV [avneesh.yadav@iiitg.ac.in](mailto:avneesh.yadav@iiitg.ac.in)

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EDUCATION

|  |  |  |
| --- | --- | --- |
| Degree | Institute/Board | CGPA/Percentage |
| B.Tech (ECE) | Indian Institute of Information Technology ,Guwahati | 7.50 Till 5th sem |
| Intermediate | U.P board | 78% |
| High School | CBSE | 10 CGPA |

SKILL

Verilog, C Programming Language

COURSEWORK

Computer programming in C

Digital Design

CMOS Digital VLSI Design

Physical Design Flow

Clock Tree Synthesis

STA

Low Power Design

ACTIVITIES

Playing Cricket,

Hobbies are Watching movies , listening song

Interest are writing poem, sketching.

CERTIFICATION

Text

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PROJECT

Learning based QRS complex ECG signal for Tachycardia patients .